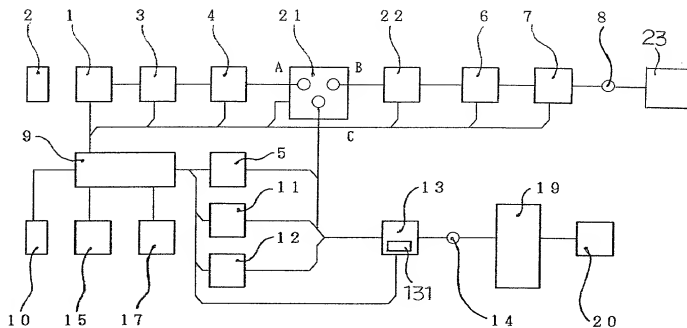


FIG. 1



- 1 IMAGE CAPTURING ELEMENT 2 LENS 3 PROCESSING UNIT 4 A/D  
 5 MEMORY 6 PROCESS CIRCUIT 7 D/A 8 TERMINAL  
 9 CONTROL CIRCUIT 10 FREEZE/THROUGH SELECTOR SWITCH  
 11 ENCODER/DECODER CIRCUIT 12 MEMORY  
 13 EXTERNAL INTERFACE CIRCUIT 14 TERMINAL 15 RECORD SWITCH  
 17 REPRODUCTION SWITCH 19 COMMUNICATION MEDIUM  
 20 EXTERNAL APPARATUS 21 SWITCH 22 DISPLAY MEMORY  
 23 DISPLAY DEVICE 131 MEMORY

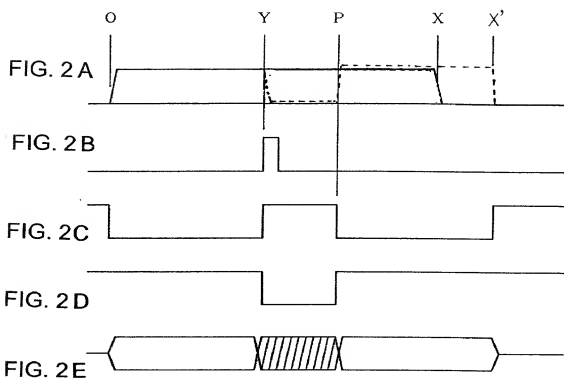
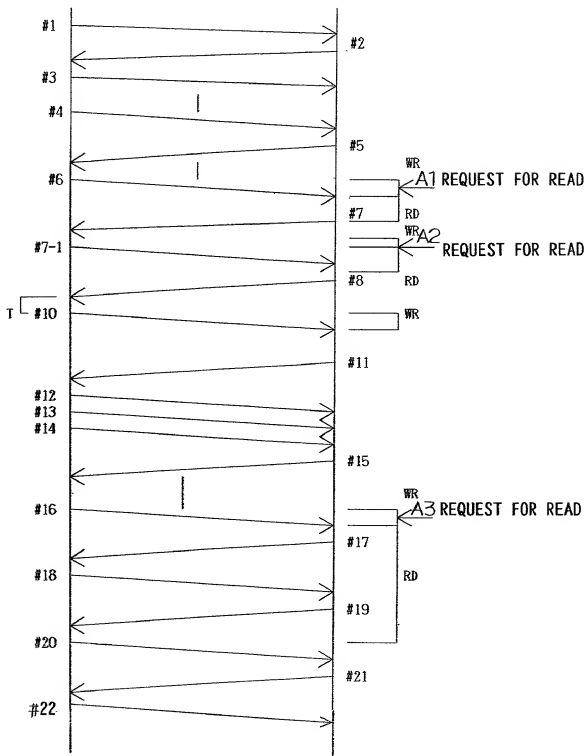


FIG. 3

EXTERNAL DEVICE 20

IMAGE CAPTURING SYSTEM



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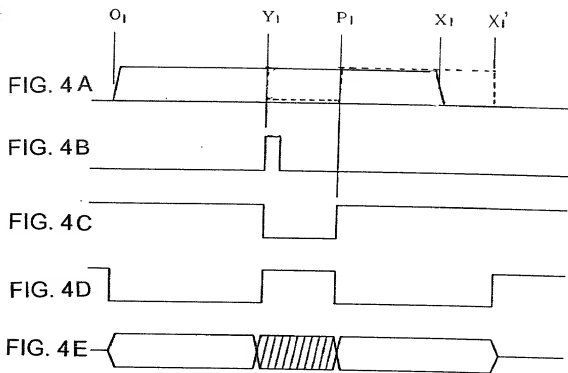
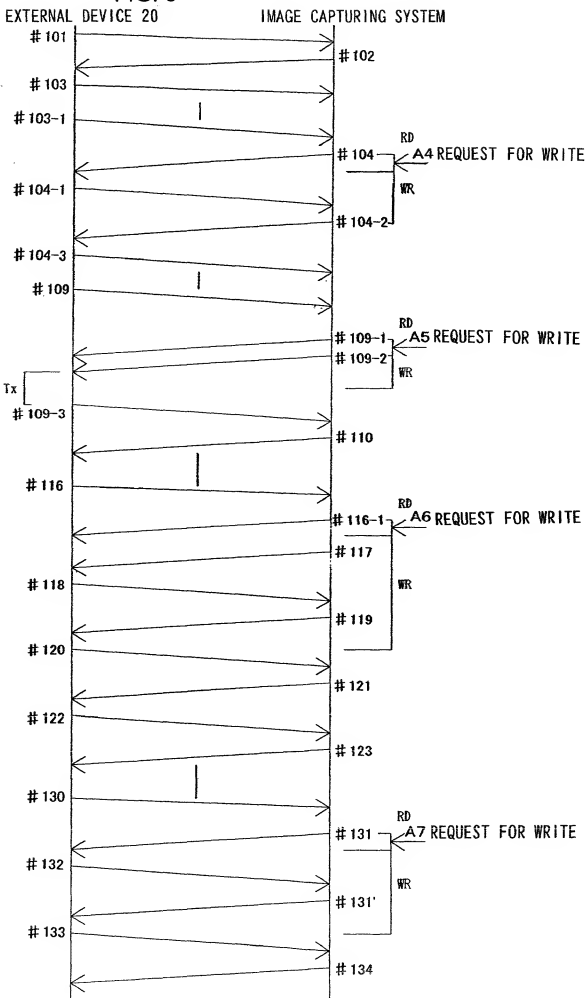


FIG. 5



09874964.100301

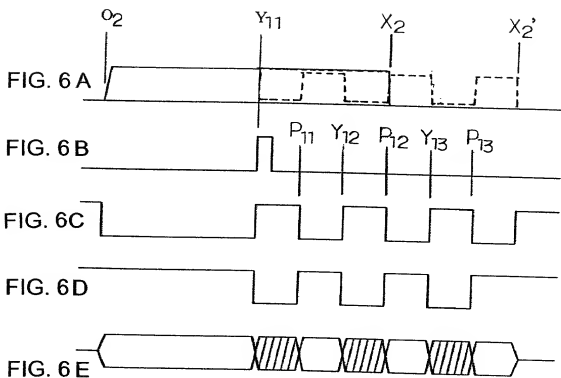


FIG. 7

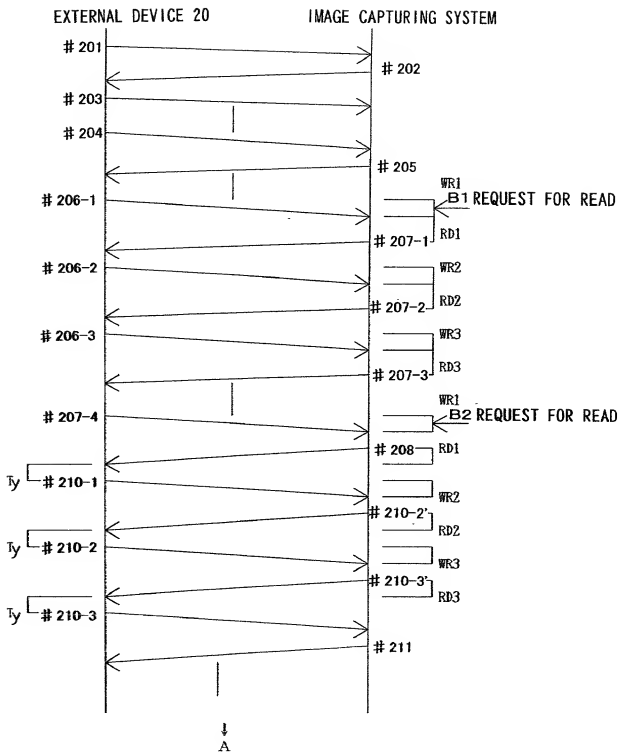
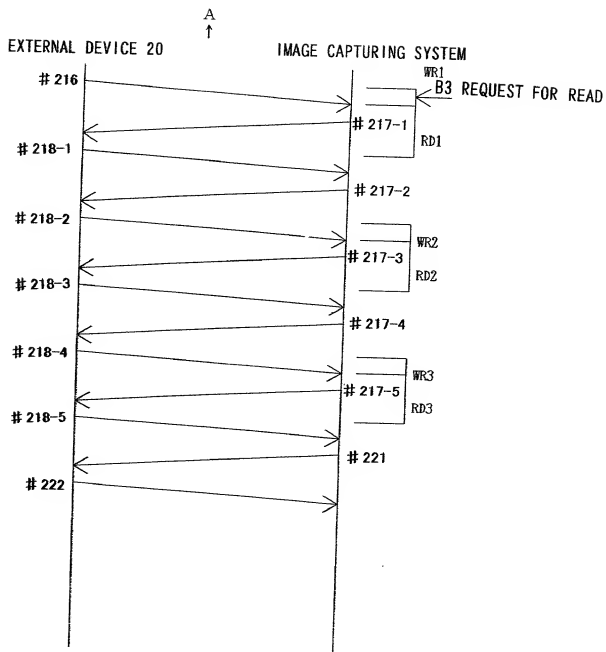


FIG. 8



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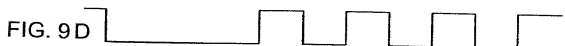
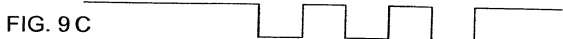
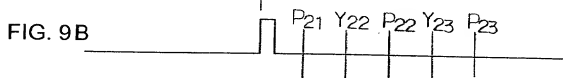
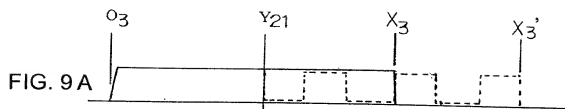
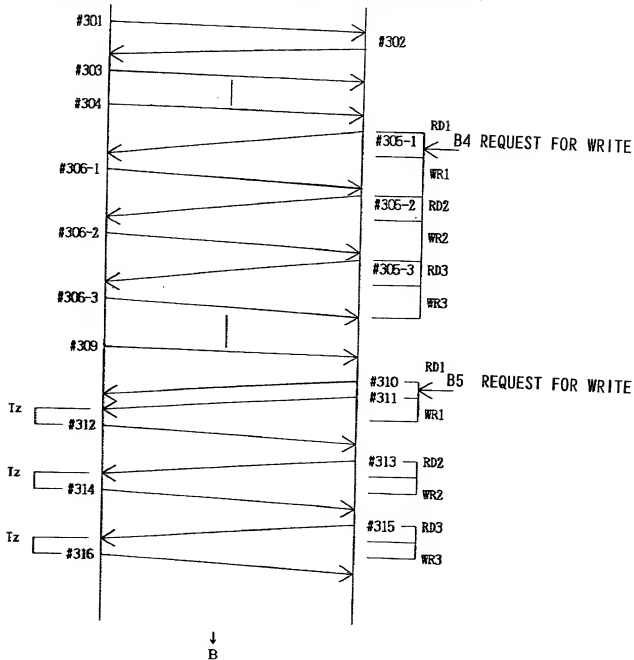


FIG. 10

EXTERNAL DEVICE 20

IMAGE CAPTURING SYSTEM



09874954.100001

FIG. 11

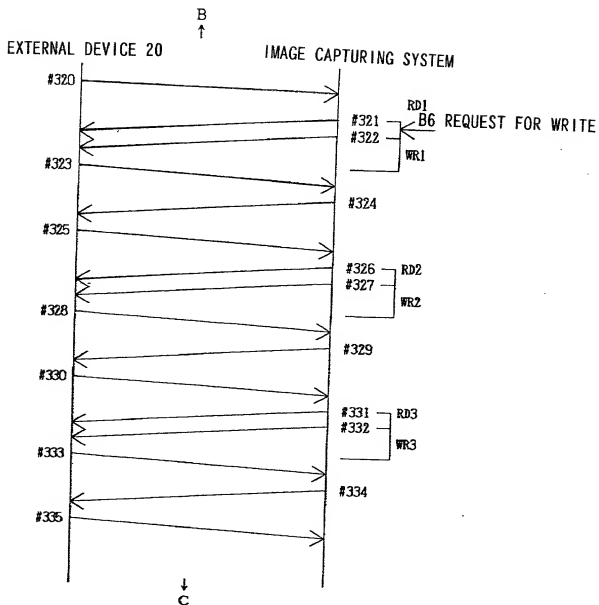
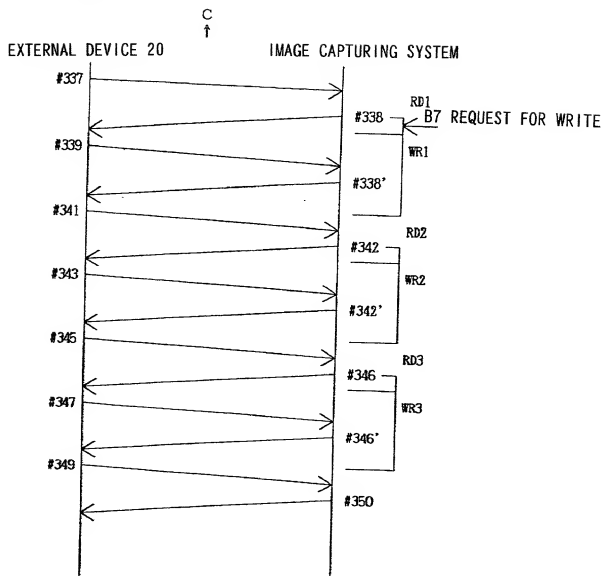


FIG. 12



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